

FIG. 2

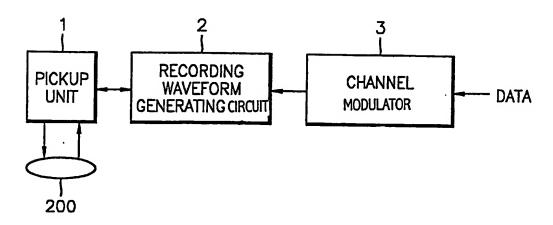
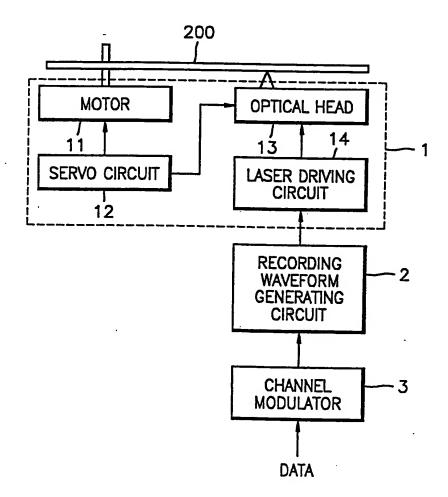
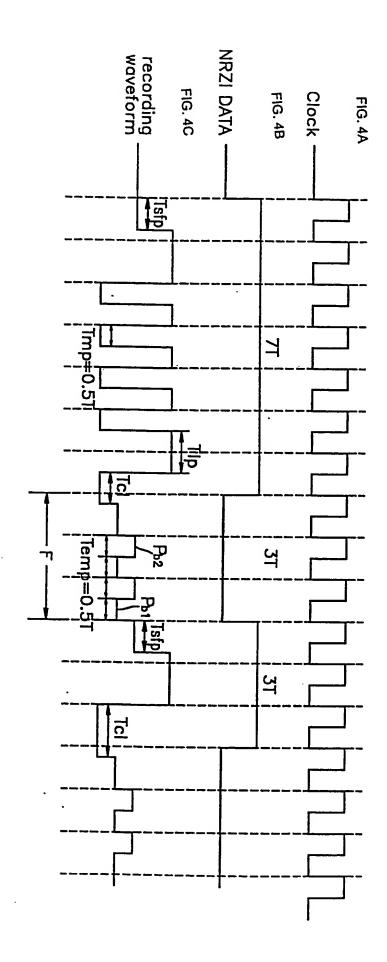
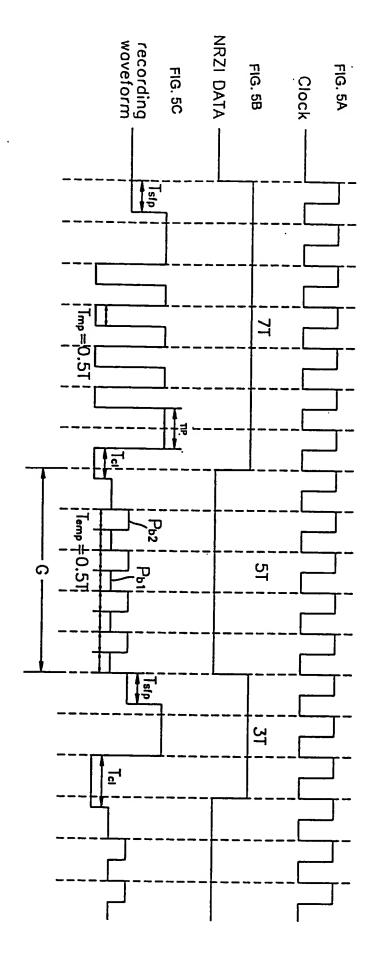
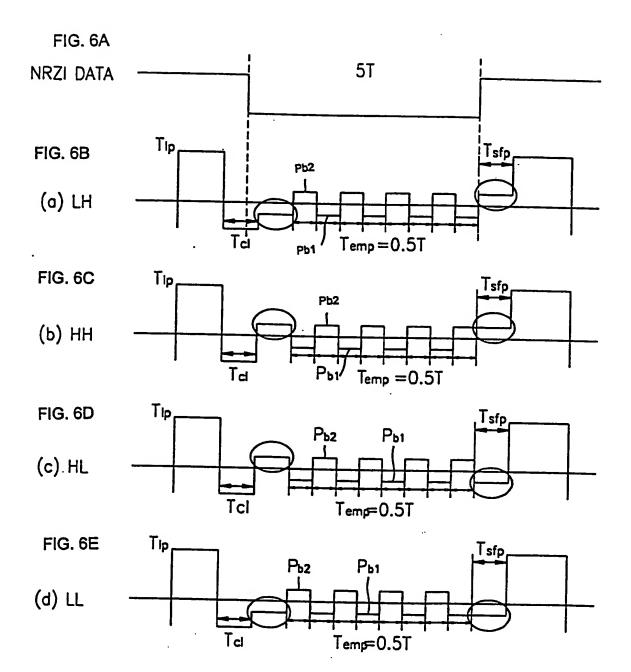


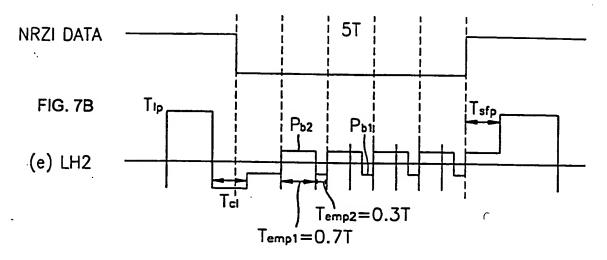
FIG. 3

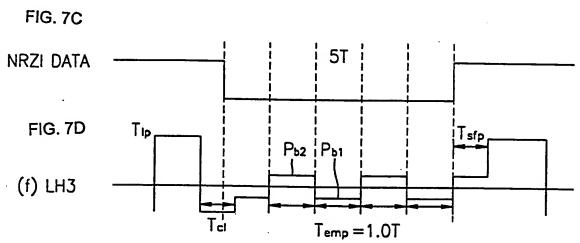












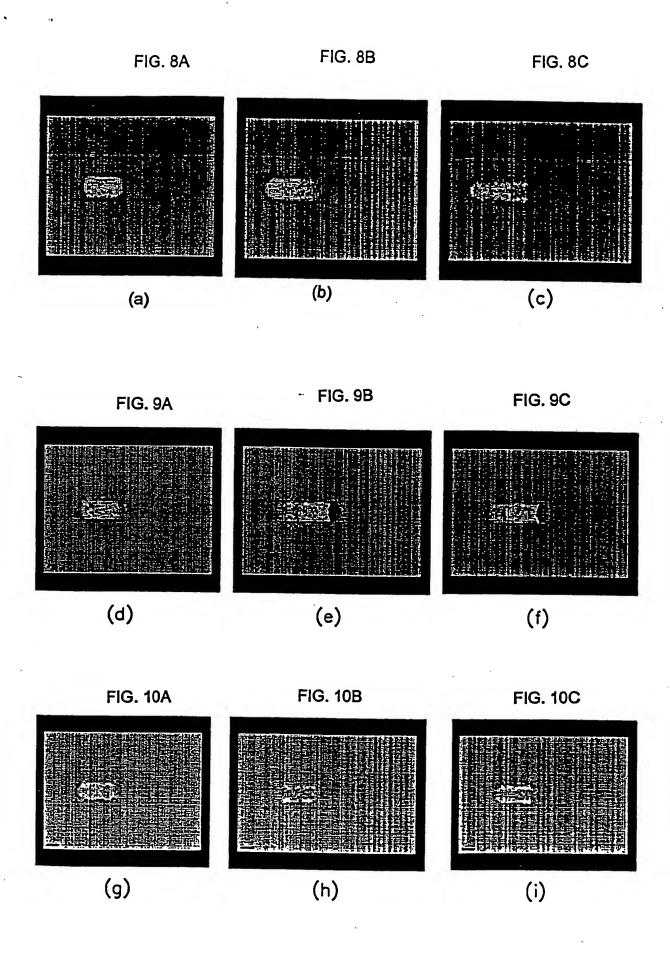
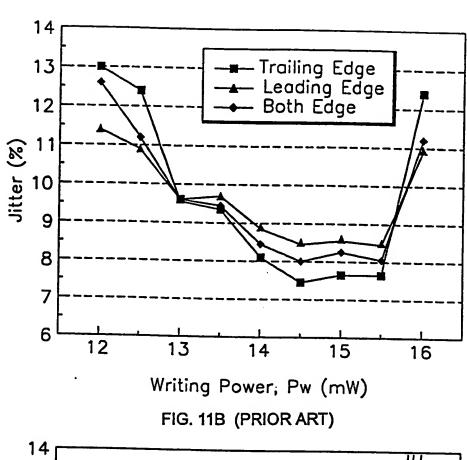
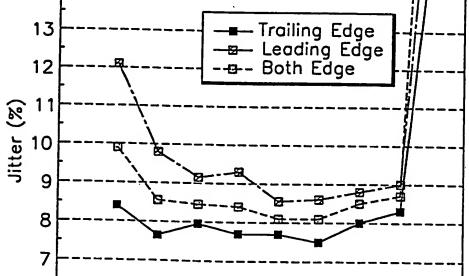


FIG. 11A (PRIOR ART)

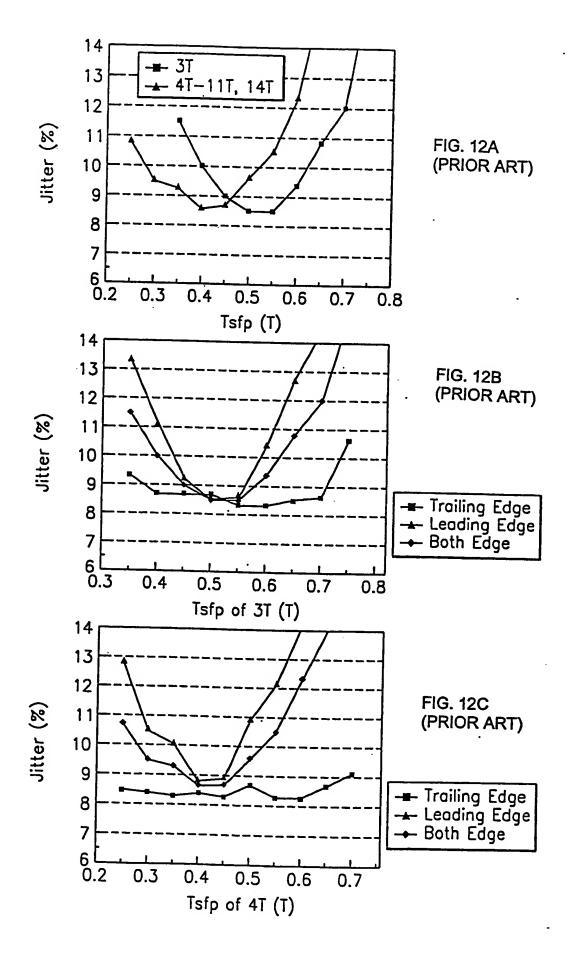


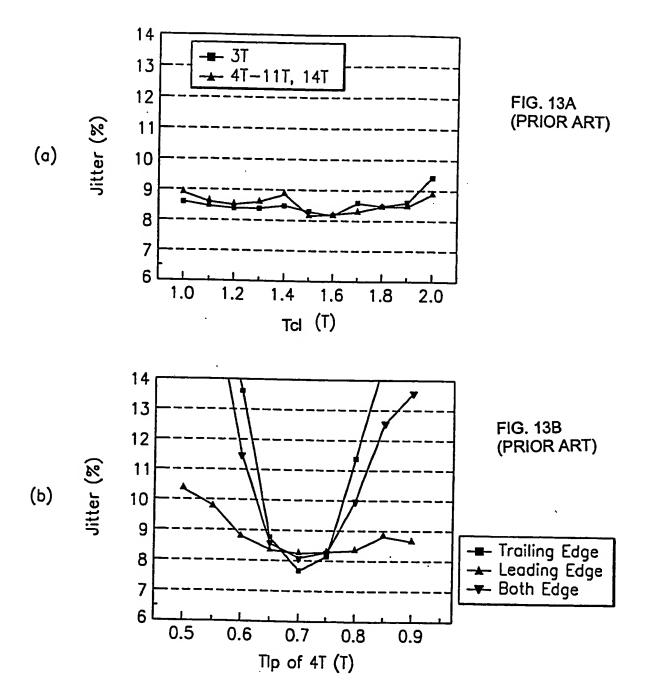


Erase Power, Pe(mW)

8

6







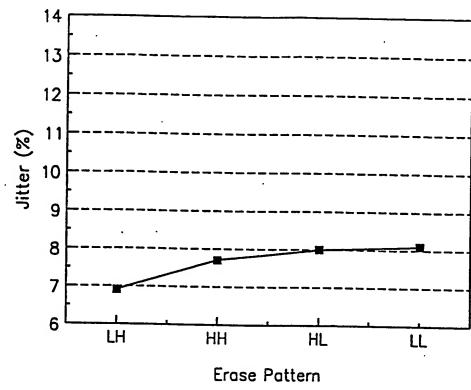


FIG. 14B

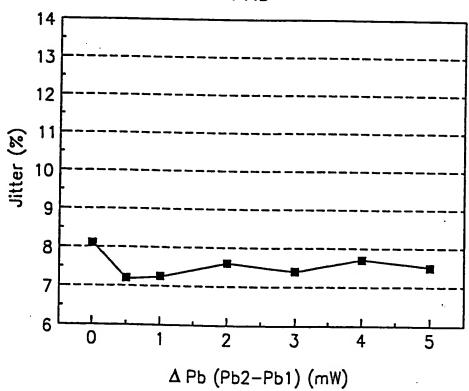
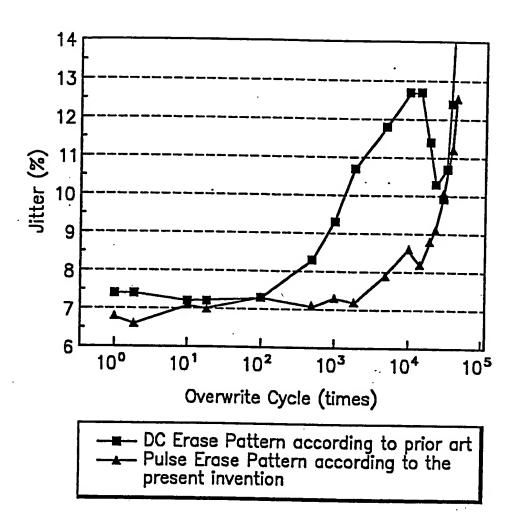


FIG. 15



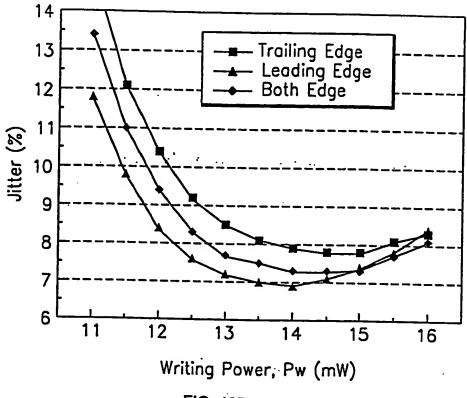
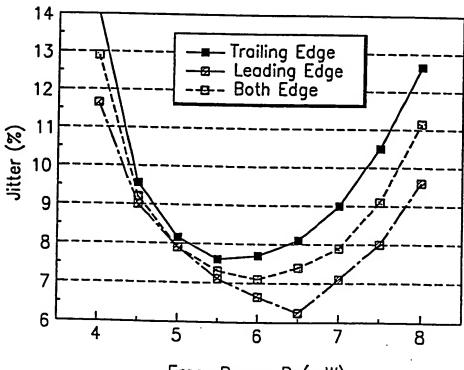
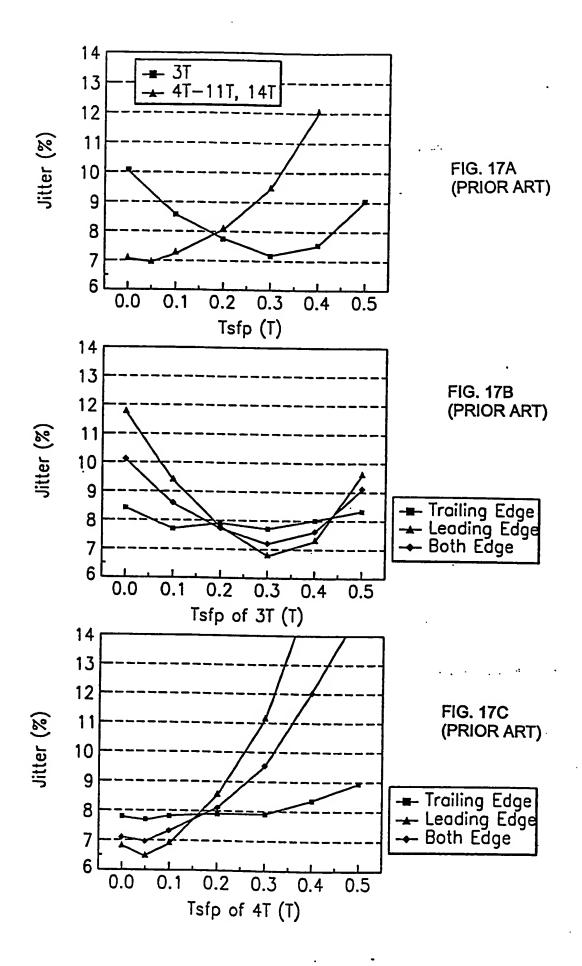
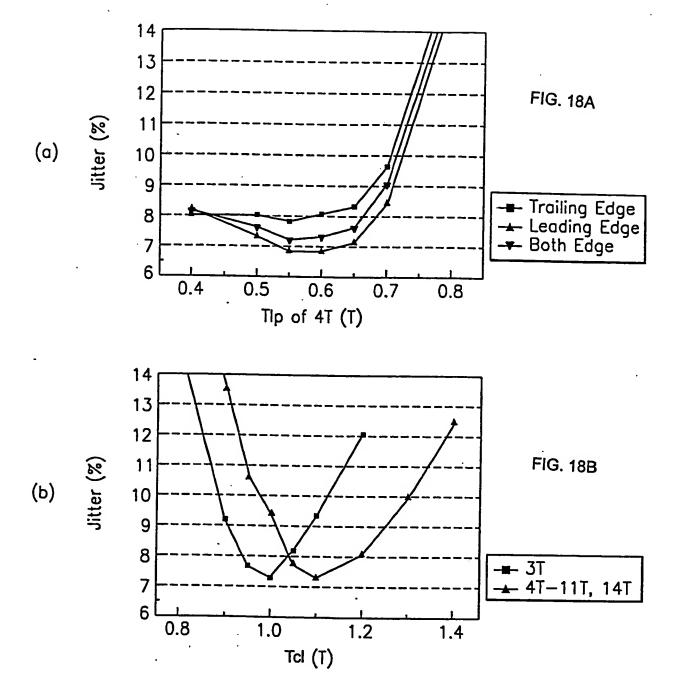


FIG. 16B (PRIOR ART)

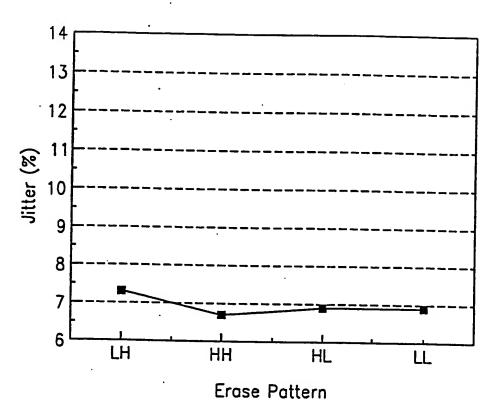


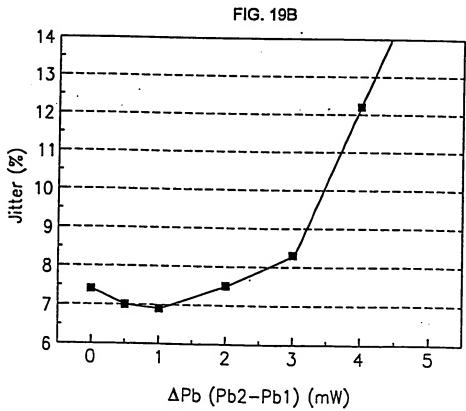
Erase Power, Pe(mW)

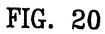


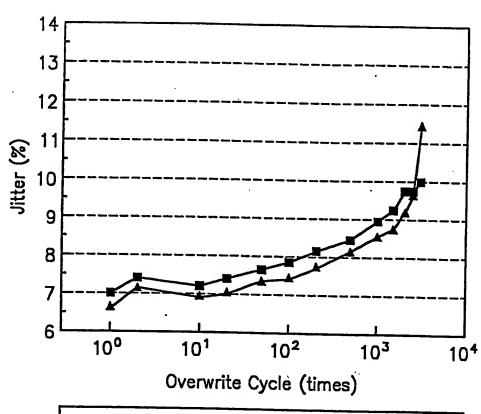












DC Erase Pattern according to prior art
→ Pulse Erase Pattern according to the present invention

FIG. 21

